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U.S. PATENT AND TRADEMARK OFFICE

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Attorney Docket No. 35640/36899
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Matsumoto Toshiyuki et al. Conf. No.: 2544
Serial No.: 09/703,845 Art Unit: 2829
Filed: November 2, 2000 Examiner: Nguyen, Tung X.
For: CAPACITANCE MEASUREMENT METHOD OF MICRO
STRUCTURES OF INTEGRATED CIRCUITS

SUBMISSION OF FORMAL DRAWINGS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

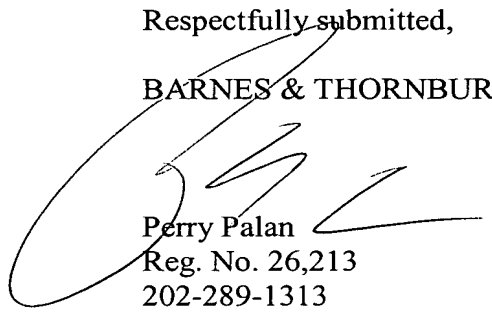
Sir:

Formal drawings are hereby submitted.

It is respectfully requested that, if necessary to effect a timely response, this paper be considered as a Petition for an Extension of Time sufficient to effect a timely response and shortages in any fees be charged, or overpayment in any fees be credited, to the Account of Barnes & Thornburg, Deposit Account No. 02-1010 (35640/36899).

Respectfully submitted,

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Enclosures